

PATENT ABSTRACTS OF JAPAN

S63-022154

(11)Publication number : 58-015473

(43)Date of publication of application : 28.01.1983

(51)Int.Cl.

H02P 1/18

H02P 3/06

H02P 7/28

(21)Application number : 56-111601

(71)Applicant : TOKYO ELECTRIC CO LTD

(22)Date of filing : 17.07.1981

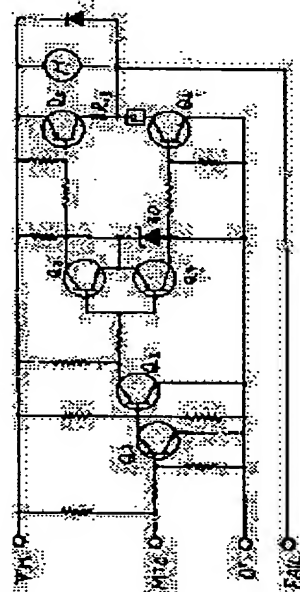
(72)Inventor : FUJII NOBUHIKO

(54) DC MOTOR DRIVE CIRCUIT

(57)Abstract:

PURPOSE: To prevent the simultaneous operation of a start circuit and a stop circuit at the time of switching control signals ON and OFF by displacing the operating timings of a motor drive power transistor and a brake power transistor.

CONSTITUTION: When a motor is started, a control signal MTC becomes LOW level, and a transistor Q2 is conducted. Since Zener diode voltage is applied to a transistor Q4, the latter is forwardly biased and is conducted, while a transistor Q3 is interrupted. When the transistor Q4 is conducted, a transistor Q6 is conducted, thereby starting a motor M. When the transistor Q3 is interrupted, a transistor Q5 is not conducted, thereby causing a brake circuit not to be operated.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office